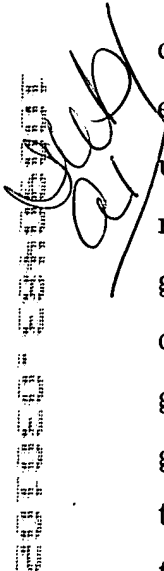


SEMICONDUCTOR INTEGRATED CIRCUIT FOR SUCCESSIVELY
SCANNING LINES OF ELECTRODES OF AN IMAGE DISPLAY
APPARATUS

ABSTRACT

An image display apparatus having a semiconductor integrated circuit for successively scanning lines of scanning electrodes without requiring conversion of the image data even in a circuit layout where scanning electrodes are distributed left and right to increase the number of pixels per unit are, the semiconductor integrated circuit comprising a storage device that receives and stores image data, a display signal generation device that generates a plurality of display signals, a first scanning signal generation device that successively generates scanning signals to be supplied to a first group of scanning electrodes based on a clock signal, a second scanning signal generation device that successively generates scanning signals to be supplied to a second group of scanning electrodes based on the clock signal, and a timing control device that generates the clock signal and generates first and second timing control signals such that the first scanning signal generation device and the second scanning signal generation device generate the scanning signals in a specified order.